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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/537,348	01/04/2006	Thierry Kretz	273516US2PCT	3464
22850	7590	09/29/2008		
OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314				
EXAMINER				
KIM, RICHARD H				
ART UNIT		PAPER NUMBER		
2871				
NOTIFICATION DATE		DELIVERY MODE		
09/29/2008		ELECTRONIC		

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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### Office Action Summary

**Application No.**

10/537,348

**Applicant(s)**

KRETZ ET AL.

**Examiner**

RICHARD H. KIM

**Art Unit**

2871

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 01 July 2005.  
2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.  
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 15-32 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.  
6) ☒ Claim(s) 15-19 and 25-28 is/are rejected.  
7) ☒ Claim(s) 20-24 and 29-32 is/are objected to.  
8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.  
10) ☒ The drawing(s) filed on 01 July 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)  
2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  
3) ☒ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date 6/17/08, 6/6/08, 9/2/05  
4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date: \_\_\_\_\_  
5) ☐ Notice of Informal Patent Application  
6) ☐ Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 15-19 and 25, 27 and 28 rejected under 35 U.S.C. 102(b) as being anticipated by Sakamoto et al. (US 5,286,983).
3. Referring to claims 15 and 27, Sakamoto discloses a display screen comprising an active matrix structure comprising pixel electrodes (38) arranged in row and columns; a switching device (36) associated with each electrode, and corresponding row selection lines (32), each selection line being disposed between two rows of successive pixel electrodes, the selection lines and electrode pixels being realized on a different level of a structure (Fig. 8, ref. 38, 58); and under each row of pixel electrode, a bus (46) made of transparent material, substantially with a same width as the row, produced on a level of the structure separated from a level of the selection lines and from a level of the pixel electrodes by at least one insulation layer (62) and connected to the selection line of a previous row of pixel electrodes, the bus forming a storage capacitor with each pixel electrode of the row (Fig. 6-8, ref. 32, 46, 38; col. 4, line 35-col. 5, line 65).
4. Referring to claim 16, Sakamoto discloses the device wherein each storage capacitor bus (46) is connected to a previous row selection line (32) outside an active zone of the matrix, at at least one end (col. 5, lines 29-35).

5. Referring to claim 17, Sakamoto discloses the device wherein each storage capacitor bus is connected at its two ends to the previous row selection line, outside the active zone (Fig. 6, ref. 32, 46).
6. Referring to claim 18, Sakamoto discloses the device wherein each storage capacitor bus is connected to the row selection line at the level of each pixel element of the associated row (Fig. 9b, ref. 32a, 46).
7. Referring to claim 19, Sakamoto discloses the device wherein the switching devices are transistors (36), the selection line of a row forming a gate (58) for each of the transistors of the row.
8. Referring to claim 25, Sakamoto discloses the device in Figure 8, wherein the storage capacitor (46) is produced on a level disposed above the levels of the selection lines (58) and the data lines (34).
9. Referring to claim 28, Sakamoto discloses the device wherein the switching devices are transistors (36), the selection line of a row (32) forming gate (58) for each of the transistors of the row, and wherein the row selection lines are driven by a line addressing signal of a pulse type having plural voltage levels (col. 2, lines 41-50).

***Claim Rejections - 35 USC § 103***

10. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

11. Claim 26 rejected under 35 U.S.C. 103(a) as being unpatentable over Sakamoto et al.
12. Sakamoto discloses the device previously recited, but fails to disclose that the storage capacitor is produced on a level disposed below the levels of the selection lines and the data line, directly on a substrate, or on an optical mask level.
13. It would have been obvious to one having ordinary skill in the art at the time the invention was made for the storage capacitor to be produced on a level disposed below the levels of the selection lines and the data line, directly on a substrate, or on an optical mask level since it has been held that rearranging part of an invention involves only routine skill in the art. *In re Japikse*, 86 USPQ 70.

#### ***Allowable Subject Matter***

14. Claims 20-24 and 29-32 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to RICHARD H. KIM whose telephone number is (571)272-2294. The examiner can normally be reached on 9:00-6:30 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571)272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Richard H Kim/  
Primary Examiner, Art Unit 2871